

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (original) A method for initiating a computer system through a memory card, wherein said computer system comprises a memory card reading device, a control circuit, and a basic input-output system memory, comprising steps of:

a) providing said memory card storing therein a basic input-output system;
b) inserting said memory card into said memory card reading device;
c) selecting a path of initiating said computer system through said memory card;

d) disabling said basic input-output system memory by said control circuit;
and

e) initiating said computer system through reading said basic input-output system by said control circuit.

2. (original) The method as claimed in claim 1, wherein said control circuit is connected to a chipset of said computer system and said basic input-output

basic memory via one of a low pin count interface and a peripheral component interconnect interface.

3. (original) The method as claimed in claim 1, wherein said control circuit is controlled by a selectively initiating signal to initiate said computer system through said memory card.

4. (original) The method as claimed in claim 3, wherein said selective initiating signal is initiated by a key on a panel of said computer system.

5. (original) The method as claimed in claim 1, wherein said control circuit is electrically connected to said memory card reading device and a power supply of said computer system respectively.

6. (original) The method as claimed in claim 5, wherein said power supply is an ATX power supply and provides a standby power.

7. (original) The method as claimed in claim 5, wherein said power supply is electrically connected to a motherboard of said computer system.

8. (original) The method as claimed in claim 1, wherein said memory card is one selected from a group consisting of a secure digital card, a memory stick, and a multimedia card.

9. (original) A method for booting a computer system having a memory card reading device, and a control circuit storing a first password, comprising steps of:

- a) providing a memory card having a second password and a basic input-output system stored therein;
- b) inserting said memory card into said memory card reading device;
- c) reading said second password by said control circuit;
- d) comparing said second password with said first password; and
- e) booting said computer system by reading said basic input-output system by said control circuit while said second password and said first password are identical to each other.

10. (original) The method as claimed in claim 9, wherein said control circuit is connected to a chipset of said computer system via one of a low pin count interface and a peripheral component interconnect interface.

11. (original) The method as claimed in claim 9, wherein said control circuit is electrically connected to said memory card reading device and a power supply of said computer system respectively.

12. (original) The method as claimed in claim 11, wherein said power supply is an ATX power supply.

13. (original) The method as claimed in claim 11, wherein said power supply is electrically connected to a motherboard of said computer system.

14. (original) The method as claimed in claim 9, wherein said memory card is one selected from a group consisting of a secure digital card, a memory stick, and a multimedia card.

15. (currently amended) A method for booting a computer system having a memory card reading device, and a control circuit, comprising steps of:

- a) providing a memory card having a basic input-output system and [[a]] an operating system stored therein;
- b) inserting said memory card into said memory card reading device;

c) booting said computer system through reading said basic input-output system by said control circuit; and

d) reading said operating system through said control circuit for operating said computer system.

16. (original) The method as claimed in claim 15, wherein said control circuit is connected to a chipset of said computer system via one of a low pin count interface and a peripheral component interconnect interface.

17. (original) The method as claimed in claim 15, wherein said control circuit is electrically connected to said memory card reading device and a power supply of said computer system respectively.

18. (original) The method as claimed in claim 17, wherein said power supply is an ATX power supply and provides a standby power.

19. (original) The method as claimed in claim 17, wherein said power supply is electrically connected to a motherboard of said computer system.

Applicants: Wang et al.
Application No.: 10/624,804

20. (original) The method as claimed in claim 15, wherein said memory card is one selected from a group consisting of a secure digital card, a memory stick, and a multimedia card.